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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)				Application Number	10/059,321
				Filing Date	January 31, 2002
				First Named Inventor	Lin
				Art Unit	2812
				Examiner Name	Stanetta D. Isaac
Sheet	1	of	1	Attorney Docket Number	2001 P 14585 US

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code <sup>2</sup> (if known)			
SDI	A	US-5,291,049	03-01-1994	Morita	
SDI	B	US-5,904,538	05-18-1999	Son, et al.	
SDI	C	US-6,174,787 B1	01-18-2001	Fuller, et al.	
SDI	D	US-20020182826 A1	12-05-2002	Cheng, et al.	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>4</sup>
		Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)				
SDI	E	DE 100 62 494 A 1	05-29-2002	Infineon Technologies AG		

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
SDI	F	HOOK, et al., "The Effects of Fluorine on Parametrics and Reliability in a 0.18-µm 3.5/6.8 nm Dual Gate Oxide CMOS Technology, IEEE Transactions on Electron Devices, Vol. 48, No. 7 (July 2001) pp. 1346-1353.	

Examiner Signature	Stanetta Isaac	Date Considered	12/18/03
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